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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

11000 U.S. PTO
09/776387
02/02/01

In re Application of:

Stanley N. Protigal et al.

Serial No.: Not Yet Assigned

Filed: February 2, 2001

For: INTEGRATED CIRCUIT MODULE
HAVING ON-CHIP SURGE CAPACITORS

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2898.2US (88-070.7)

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

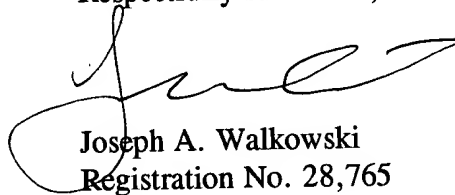
The present application is a continuation of U.S. Patent Application Serial Number 08/965,741, filed November 7, 1997, pending, which is a continuation of U.S. Patent Application Serial Number 08/671,248, filed June 27, 1996, now U.S. Patent 5,687,109, which is a continuation of U.S. Patent Application Serial Number 08/178,716, filed January 10, 1994, now abandoned, which is a continuation of U.S. Patent Application Serial Number 08/034,001, filed March 19, 1993, which is now U.S. Patent 5,307,309, which is a continuation of U.S. Patent Application Serial Number 07/774,121, filed October 8, 1991, abandoned, which is a continuation of U.S. Patent Application Serial Number 07/291,294, filed December 27, 1988, now abandoned, which is a continuation-in-part of U.S. Patent Application Serial Number 07/200,673, filed May 31, 1988, now abandoned.

Attorney Docket: 2898.2US (88-070.7)

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: February 2, 2001

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Enclosure: Form PTO-1449

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